



Teaching plan for the semester form

Course Instructor	Hassan M. Jaber Al-Ta'ii			
E_mail	domez973@yahoo.com			
Title	Digital electronics			
Course Coordinator				
Course Objective	Showing the basic elements for electronic circuits such as oscillators, amplifiers, and others, and how these elements working with the mathematical analysis to their circuits. Also the student will recognize the digital elements such: logic gates.			
Course Description	Logic circuits, Boolean algebra, Truth table and Karnauugh map, Arithmetic circuits Subtraction, The adder – subtractor, Full adder, Half adder, design of digital electronics circuits.			
Textbook	Digital fundamental (T. Floyd ,9th ed. ,2006)			
References	Electronic Physics			
Course Assessment	Term Tests	Quizzes	Project	Final Exam
	(30%)	(10%)	----	As (40%)
General Notes	Type here general notes regarding the course			



Teaching plan for the Second semester form

week	Date	Topics Covered	Lab. Experiment Assignments	Notes
1	18/2/2019	Introductory Digital Concepts		
2	25/2/2019	Number Systems		
3	4/3/2019	Number Systems		
4	11/3/2019	Number Systems		
5	18/3/2019	Subtraction and addition for different system		
6	25/3/2019	Logic gates, design the logic circuit		
7	1/4/2019	Logic gates, design the logic circuit		
8	8/4/2019	Logic gates, design the logic circuit		
9	15/4/2019	Boolean algebra, Boolean expressions		
10	22/4/2019	Boolean algebra, Boolean expressions		
11	29/4/2019	Boolean algebra, Boolean expressions		
12	6/5/2019	KARNAUGH MAP MINIMIZATION		
13	13/5/2019	KARNAUGH MAP MINIMIZATION		
14	20/5/2019	COMBINATIONAL LOGIC ANALYSIS		
15	27/5/2019	FUNCTIONS OF COMBINATIONAL LOGIC		

Instructor Signature:

Dean Signature: